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CONFIRMATION NO. 4741

Bib Data Sheet						
SERIAL NUMBER 10/613,246	FILING DATE 07/03/2003 RULE	CLASS 711	GROUP ART UN 2188	IIT A	TTORNEY DOCKET NO. 5646-42XX	
APPLICANTS						
Mario Au, Fremont, CA;						
Jiann-Jeng Duh, San Jose, CA;						
** CONTINUING DATA **********************************						
** FOREIGN APPLICATIONS ************************************						
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 10/17/2003						
Foreign Priority claimed yes no yes no S' 35 USC 119 (a-d) conditions met			SHEETS	TOTAL	INDEPENDENT	
facilities of a second Automorphism of the	Jain Molley	llowance COUNTRY CA	DRAWING 26	CLAIMS 24	S CLAIMS 7	
ADDRESS 20792 MYERS BIGEL SIBLEY & SAJOVEC PO BOX 37428 RALEIGH, NC 27627						
TITLE Integrated circuit memory devices having clock signal arbitration circuits therein and methods of performing clock signal arbitration						
FILING FEE FEE	ES: Authority has been given	in Paper	All Fees 1.16 Fees (Filing) 1.17 Fees (Processing Ext. of time)			
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